

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: James D. Ballew et al.

Serial No.: 10/824,874

Filing Date: April 15, 2004

Art Unit: 2181

Confirmation No.: 7117

Examiner: Benjamin P. Geib

Title: *Coupling Processors to Each Other for High performance
Computing (HPC)*

MAIL STOP: Amendment

Commissioner of Patents

PO Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Supplemental Information Disclosure Statement (IDS)


Applicants respectfully request, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the reference listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. Pursuant to 37 C.F.R. §§1.97(g) and (h), Applicants make no representation that this document qualifies as prior art, that this document is material to patentability of the present application, or that a search has been made.

References other than U.S. patents and U.S. patent application publications are enclosed for the convenience of the Examiner.

This Supplemental IDS is being submitted before the first Office Action in the above-identified patent application (after the filing of an RCE). Although Applicants believe no fees are due, the Commissioner is hereby authorized to charge any necessary additional fees and credit any overpayments to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,

Baker Botts L.L.P.
Attorneys for Applicants



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Date: January 26, 2011

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